

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|--|------------------|---------|------------------|
| L1 | 2413 | (pin\$1 or pad\$1) and ((input/output adj1 cell\$1) or (i/o adj1 cell\$1) or (input NEAR1 output NEAR1 cell\$1) or (input/output adj1 block\$1) or (i/o adj1 block\$1) or (input NEAR1 output NEAR1 block\$1) or (input/output adj1 circuit\$1) or (i/o adj1 circuit\$1) or (input NEAR1 output NEAR1 circuit\$1)) and ((programmable adj1 logic) or (programmable adj2 device) or (programmable adj2 circuit) or plid or fpga) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/26 09:15 |
| L2 | 1057 | 1 and ((logic adj2 circuit) or (functional adj circuit) or (logic adj1 module) or (functional adj1 module)) and mode\$1 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/26 09:41 |
| L3 | 63 | 2 and ((general NEAR1 purpose NEAR1 interface) or (general NEAR1 purpose NEAR1 input/output) or (general NEAR1 purpose NEAR1 input NEAR1 output)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/26 09:44 |

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|----------|------------------|---------|------------------|
| L1 | 22 | ((pin\$1 or pad\$1) and ((input/output NEAR2 cell\$1) or (i/o NEAR2 cell\$1) or (input/output NEAR2 block\$1) or (i/o NEAR2 block) or (input/output NEAR2 circuit) or (i/o NEAR2 circuit) or (input NEAR2 output NEAR2 circuit) or (input NEAR2 output NEAR2 cell\$1) or (input NEAR2 output NEAR2 block\$1)) and ((programmable NEAR1 logic) or (programmable NEAR1 circuit) or (programmable NEAR2 device)) and (mode\$1 or select\$4)).clm. | US-PGPUB | OR | ON | 2005/08/26 10:06 |